



California State University, Sacramento
College of Engineering and Computer Science

CSC 35: Introduction to Computer Architecture

Summer 2026 – Tentative Schedule

Depending on how fast/slow we go on some subjects, this schedule might have to be revised.

| | Date | Lecture | Lab |
|---|--------|----------------------------------|--------------------------|
| 1 | 6 / 1 | Data | |
| | 6 / 3 | Processors | |
| | 6 / 5 | Programs | |
| 2 | 6 / 8 | Control Logic – If Statement | Lab 1: Hello World |
| | 6 / 10 | CANCELED | |
| | 6 / 12 | Control logic – Switch and Loops | Lab 2: Conditional Logic |
| 3 | 6 / 15 | ALU – Signed, multiply | Lab 3: If Statements |
| | 6 / 17 | ALU – Divide | Lab 4: Loops |
| | 6 / 19 | Holiday – Campus Closed | |
| 4 | 6 / 22 | Midterm | |
| | 6 / 24 | Memory | Lab 5: Mathematics |
| | 6 / 26 | Indexing | Lab 6: Direct Storage |
| 5 | 6 / 29 | Subroutines & Operating Systems | Lab 7: Indexing |
| | 7 / 1 | Design Principles | Lab 8: Kernal Calls |
| | 7 / 3 | Holiday – Campus Closed | |
| 6 | 7 / 6 | Real-World Encodings | |
| | 7 / 8 | Pipelining & Review | |
| | 7 / 10 | Final | |